Cache Lab Q&A

We need a four-byte word from ROM. If offset is 0, which byte do we expect?

The rightmost byte \( 0x \text{FFFFFF} 88 22 \)

Graduation

Datapath 4 at today after class
Datapath 5 out this weekend
maybe also #6 too
Cache lab out next week

13.5 labs total

Welcome counts as half
Datapath I&II counts as two labs
Intel 8800 vs. 8086

8080 - 8-bit microprocessor
replace w/ 8800 - new architecture, lots of nice features, not compatible w/ 8080
rushed to create 8086 instead → IBM PC

→ 80186 → 80286 → 80386 → 80486 → 80586 → 80686
   32 bit

→ AMD64/X86_64  AMD did this one
while Intel was working. Pentium → “Titanic”

Complex/Reduced Instruction Set Computer

CISC X86
many instructions, very specialized
one instruction does more work

RISC MIPS
few instructions, very general/simple
one instruction does less work
End of course evaluations

https://grinnell.smartevals.com

Please write comments!