

Cache Lab Q&A

We read a four-byte word from ROM. If offset is 0, which byte do we expect?

The rightmost byte

0x FF CC 88 22
offset 0

Grading

Datapath 4 out today after class

Datapath 5 out this weekend

maybe also #6 too

Cache lab out next week

13.5 labs total

Welcome counts as half

Datapath I&II counts as two labs

Hennessey and Patterson - CACM

Intel 8800 vs. 8086

8080 - 8-bit microprocessor

replace w/ 8800 - new architecture, lots of nice features, not compatible w/ 8080

rushed to create 8086 instead → IBM PC

→ 8186 → 8286 → 8386 → 8486 → 8586 → 8686
32 bit Pentium

→ AMD64/x86_64 AMD did this one

while intel was working Itanium → "Itanic"

Complex/Reduced Instruction-Set Computer

CISC x86

many instructions, very specialized

one instruction does more work

RISC MIPS

few instructions, very general/simple

one instruction does less work

WINNER
OF THE
PULITZER
PRIZE

**THE SOUL
OF A NEW MACHINE**

TRACY KIDDER

"RIVETING...A WELL-PACED
STORY OF CORPORATE INVENTION
AND INTRIGUE...COMPELLING
ENTERTAINMENT AND MUCH MORE."
—WASHINGTON POST BOOK WORLD

NATIONAL BESTSELLER

End of course evaluations

<https://grinnell.smartevals.com>

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